



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/556,721	11/14/2005	Tsuneo Shirai	279719US6PCT	3287
22850	7590	06/16/2009		
OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C.				EXAMINER
1940 DUKE STREET				FOSELMAN, JOEL, W
ALEXANDRIA, VA 22314				ART UNIT
				PAPER NUMBER
				2622
			NOTIFICATION DATE	DELIVERY MODE
			06/16/2009	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

patentdocket@oblon.com
oblonpat@oblon.com
jgardner@oblon.com

DETAILED ACTION

Response to Amendment

The amendment filed on 05/28/2009 in response to the previous Final Office Action (03/30/2009) is acknowledged and has been entered. Claims 1,4-8,12-14 are currently pending. Claims 2-3,9-11,15 are cancelled.

Response to Arguments

Applicant's arguments with respect to claims 1,4-7 have been fully considered but are not persuasive.

Applicant alleges that the combination of Arai and Nakano fails to teach or suggest a superimposing unit, in which, the control data being composed of a plurality of data elements, each data element being composed of a data byte that is repeated multiple times with each clock signal of the image processing apparatus to generate a repetitive data element series for each data element (Remarks, p 7). Applicant further explains that Nakano does not teach such a feature, because he merely sends two 24-bit data words twice, by repeating the entire 24-bit data set (Remarks, p 8). Examiner respectfully disagrees.

As cited by applicant in the remarks, Nakano explains that the command data set is sent twice in series to the receiver section 206 of display device 200, in synchronization with a clock signal and a synchronization signal (Remarks, p 8). Additionally, Nakano sends two identical 24-bit words twice. In other words, Nakano sends a control signal comprising 3-bytes (24-bits) twice, which is, in fact a plurality of

times, as claimed, to generate a repetitive data element series for each data element. Since the signal is sent twice each byte is also sent twice, hence repetitive data for each data element.

Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JOEL FOSSELMAN whose telephone number is (571)270-3728. The examiner can normally be reached on 9:00 AM - 6:00 PM M-F, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan can be reached on (571) 272-3022. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Joel Fosselman/
Examiner, Art Unit 2622

/Jason Chan/
Supervisory Patent Examiner, Art Unit 2622